

Note: N_{out} increases by a factor of $\sqrt{2}$.
 But signal power also increases by a factor of 2.
 \Rightarrow overall S/N increases by a factor of $\sqrt{2}$.

$$4) (V_{GS} - V_{th}) \uparrow \times 2 \longrightarrow (W/L) \downarrow \times \frac{1}{2}$$

(why: To maintain same current)

$$\rightarrow g_m = \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{th}) \downarrow \times \frac{1}{2}$$

- g_m decreases by a factor of 2.
- \rightarrow Noise power will be decreased by a factor of 2
- \rightarrow signal power will be decreased by a factor of 4.

$$\therefore (S/N)_{out} \downarrow \times \frac{1}{2}$$

$$\cdot HD_2 \downarrow \times \frac{1}{2}$$

NOTE:

From 3) and 4), you can reason that
 Under constant bias current,

- ① To achieve better NF \rightarrow Increase W/L
- ② To achieve better linearity \rightarrow Increase $(V_{GS} - V_{th})$.

~~small current density~~
~~per width~~

~~large current density~~
~~per width~~

A few more remarks

- ① Increasing W/L with constant I_{bias} means
 "small current density" _{per width} for low-noise operation
- ② Increasing $(V_{GS} - V_{th})$ with constant I_{bias} means
 "large current density" _{per width} for high-linearity operation.