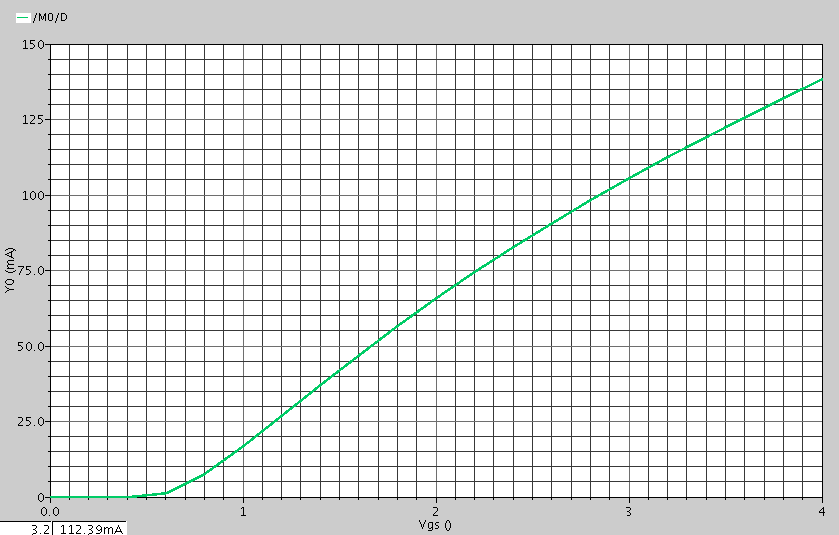
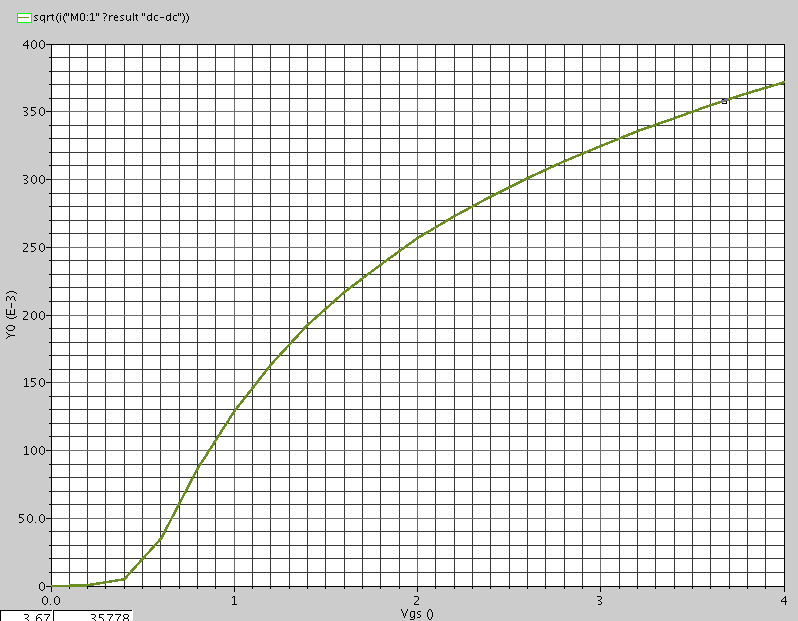
1. Scematic:
2. curve



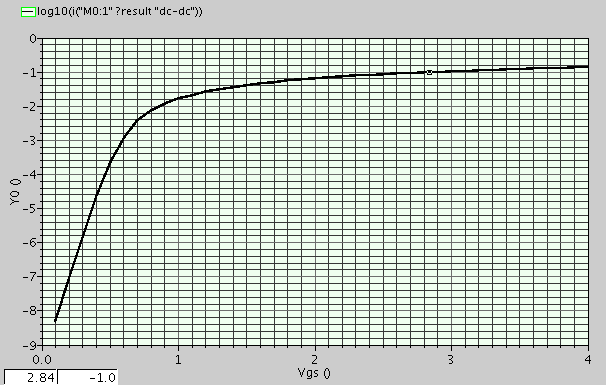
(b)



(c)



(d) vs

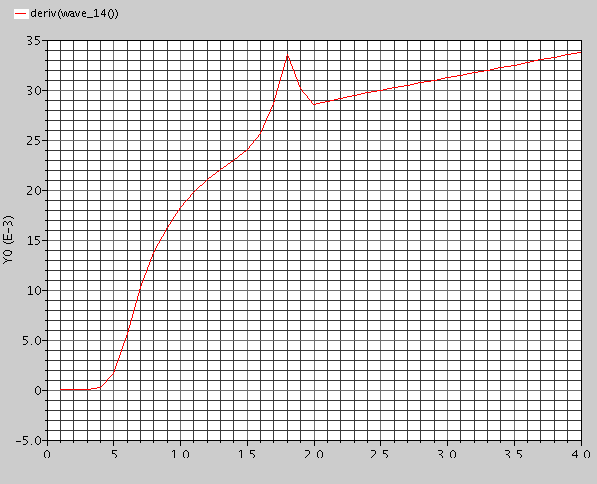


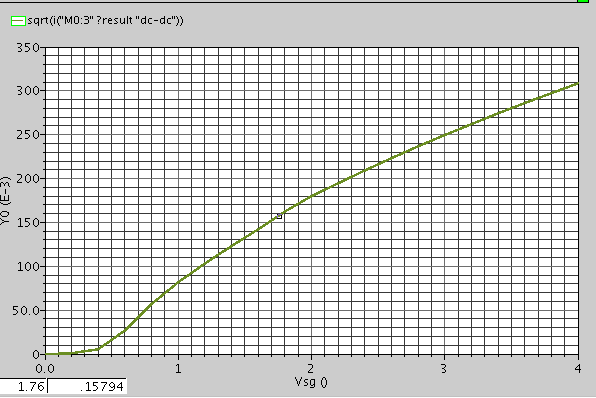
(e) Using PFET (using three terminal PFET)

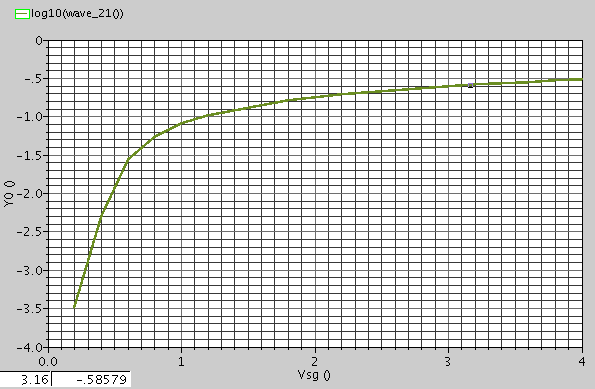
Diagram:

:

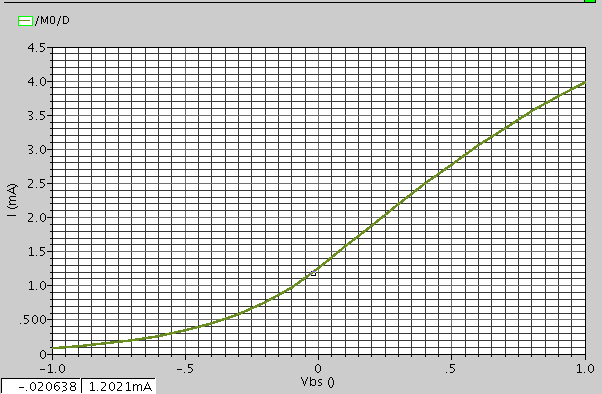


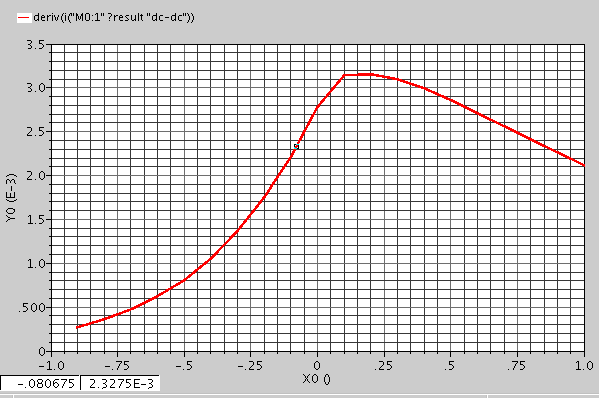




vs 

1. curve

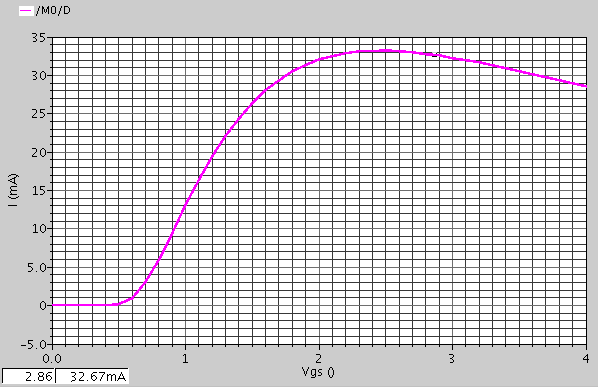




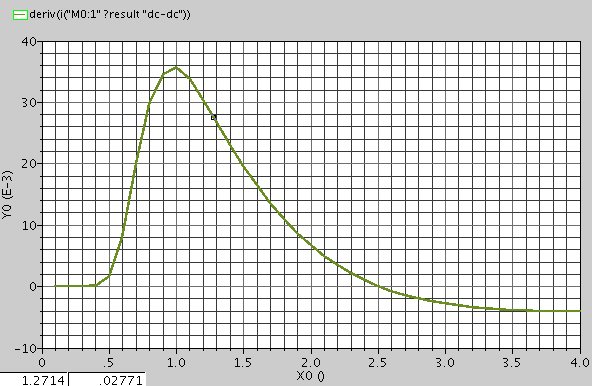
**Problem 2:**

Schematic:

a) curve



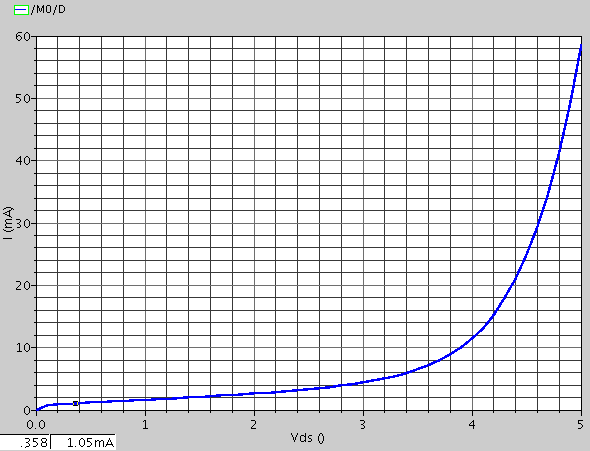
b)



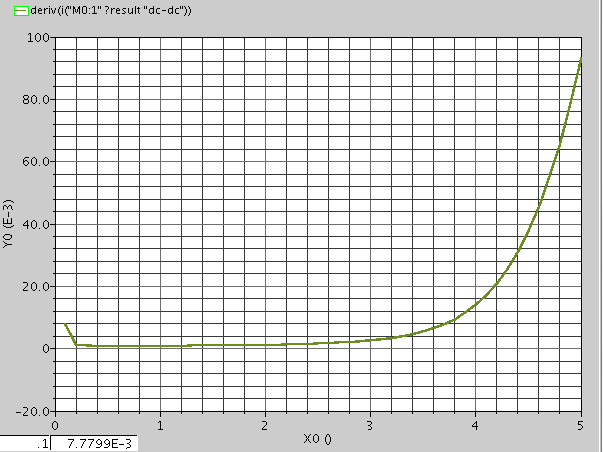
c) vs



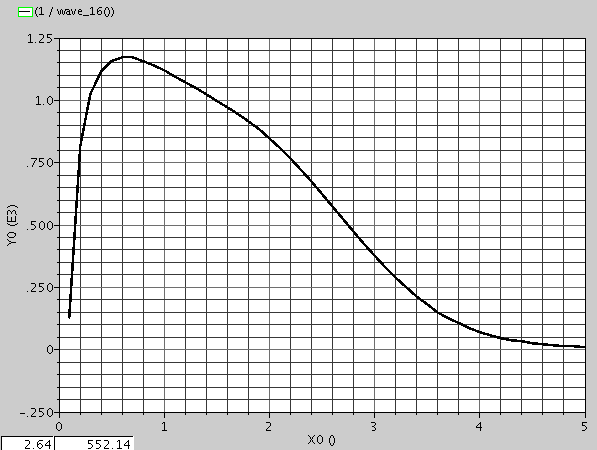
d) curve



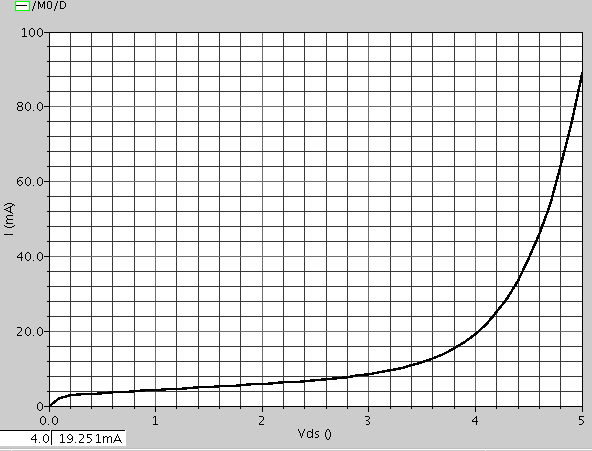
e)

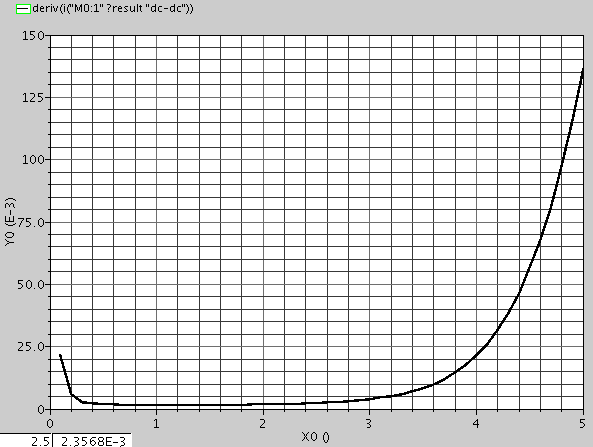


vs

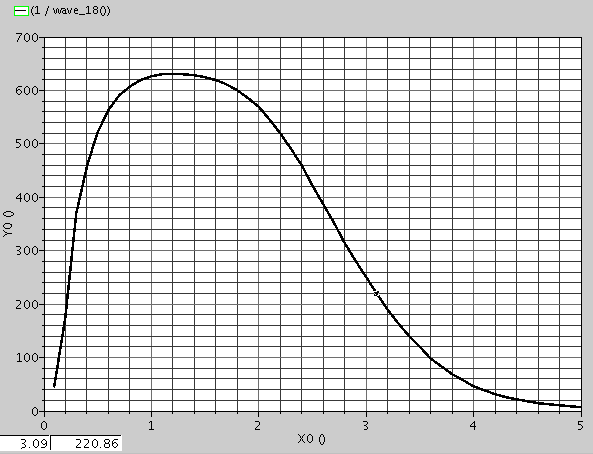


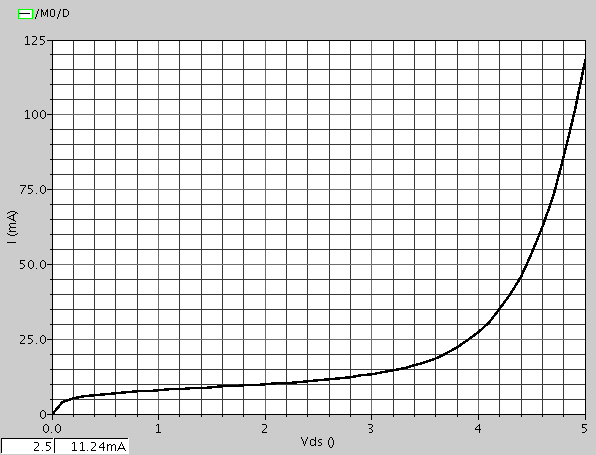
f) curve

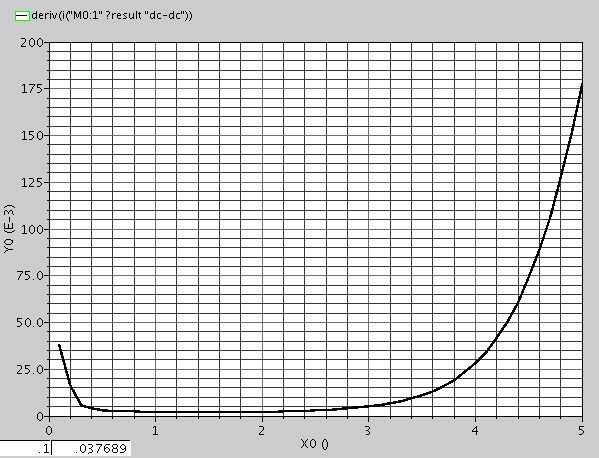




vs



g) curve 



vs ,

