Digital Logic Levels
Definitions

- $V_{CC}$ – positive power supply voltage
- $-V_{CC}$ – negative power supply voltage (usually 0V)
- $V_{oH}$ – the voltage on the output pin when the value of the logical operation is a “1”
- $V_{oL}$ – the voltage on the output pin when the value of the logical operation is a “0”
- $V_{iH}$ – the minimum voltage on the input pin that causes the logical gate to operate like there is a “1” on the input.
- $V_{iL}$ – the maximum voltage on an input pin that causes the logical gate to operate like there is a “0” on the input.
- $X$ – the range of voltages on the input pins that result in a voltage on the output pin that cannot be recognized as a “0” or a “1” on the input pin of another logical gate.
Voltage Levels

Modified from http://ics.nxp.com/support/techtips/voltage.level.translation/